

10/069053

FILED UNDER 35 U.S.C. 371

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10069053	FILING DATE 07/08/2002	CLASS 528	SUBCLASS 44	GAU 1711 1772	EXAMINER Gorr
----------------------	---------------------------	--------------	----------------	---------------------	------------------

****APPLICANTS:** Fujinawa Tohru; Yusa Masami; Nomura Satoyuki; Ono Hiroshi;
Watanabe Itsuo; Arifuku Motohiro; Kanazawa Hohiko;

****CONTINUING DATA VERIFIED:**

THIS APPLICATION IS A 371 OF PCT/JP00/05763 08/25/1999

FOREIGN APPLICATIONS VERIFIED:

JAPAN 11-238409 08/25/1999

JAPAN 2000-92978 03/28/2000

BEST AVAILABLE COPY

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 566.41199X00	
Verified and Acknowledged Examiners's initials			

TITLE : Wiring-connecting material and process for producing circuit board with the same	
U.S. DEPT. OF COMM./PAT. & TM-PTO-433L (Rev. 12-94)	

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims 21	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drawn 1	Figs. Drawn 1
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code (Title 35, Sections 122, 181, 182, 168, 351, 352, 354, 355). The U.S. Patent and Trademark Office is restricted to authorized employees and contractors only.			
FILED WITH:		DISK (CRF) <input type="checkbox"/>	CD-ROM <input type="checkbox"/>
Attached in pocket on right inside flap			